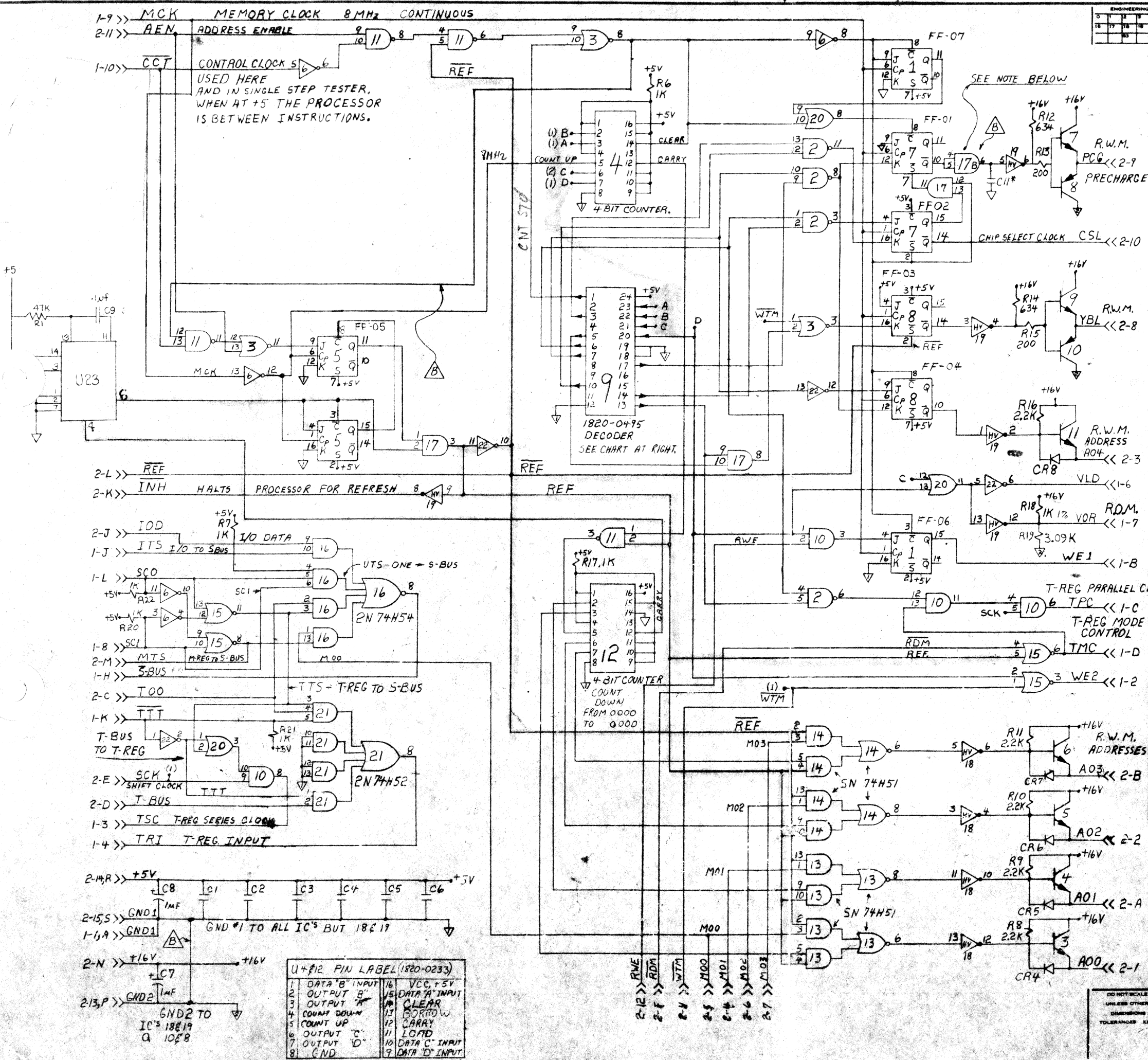


1820-0495

DECODER

INPUT				LOW
20	21	22	23	OUTPUT
0	0	0	0	PIN 1
0	0	0	1	PIN 2
0	0	1	0	PIN 3
0	0	1	1	PIN 4
0	1	0	0	PIN 5
0	1	0	1	PIN 6
0	1	1	0	PIN 7
0	1	1	1	PIN 8
1	0	0	0	PIN 9
1	0	0	1	PIN 10
1	0	1	0	PIN 11
1	0	1	1	PIN 13
1	1	0	0	PIN 14
1	1	0	1	PIN 15
1	1	1	0	PIN 16
1	1	1	1	PIN 17

IC 1, 5, 7, 8	1820-0715	Q 1, 2	1854-0351	R 12, 14	0698-4459
IC 2, 11	1820-0370	Q 3, 4, 5, 6, 11	1854-0404	R 13, 15	0757-0407
IC 3, 15	1820-0239	Q 7, 9	1854-0087	R 18	0757-0280
IC 4, 12	1820-0233	Q 8, 10	1853-0016	R 19	0698-4438
IC 6, 22	1820-0424				
IC 9	1820-0495	ALL DIODE 1901-0040			
IC 10, 17	1820-0141	R 1, 2	0757-0421		
IC 13, 14	1820-0378	R 3	0757-0415		
IC 16	1820-0381	R 4, 5	0757-0437	C 1, 2, 3, 4, 5, 6 .01µF	
IC 18, 19	1820-0471	R 6, 7, 17, 20, 21, 22	0684-1021	C 7, 8	0180-0291
IC 20	1820-0205	R 8, 9, 10, 11, 16	0684-2221	C 9, 10	0180-0218
IC 21	1820-0379				



NOTE: IC17B MUST BE A 1820-0551. THE DELAY TIME IN THIS TYPE I.C. IS REQUIRED TO ACHIEVE THE TOVL SPECIFIED BELOW. C11* WILL AFFECT 'TOVL'.

ANY CHIP SELECT LINE

TOVL: SHOULD BE 25 TO 75 NSEC. NOMINAL 50 NSEC. SET BY VALUE OF C11.

NOTE: REV A SCHEMATIC GOOD FOR MODIFIED KEYB/C BOARDS
 REV B SCHEMATIC GOOD FOR REV D BOARDS.