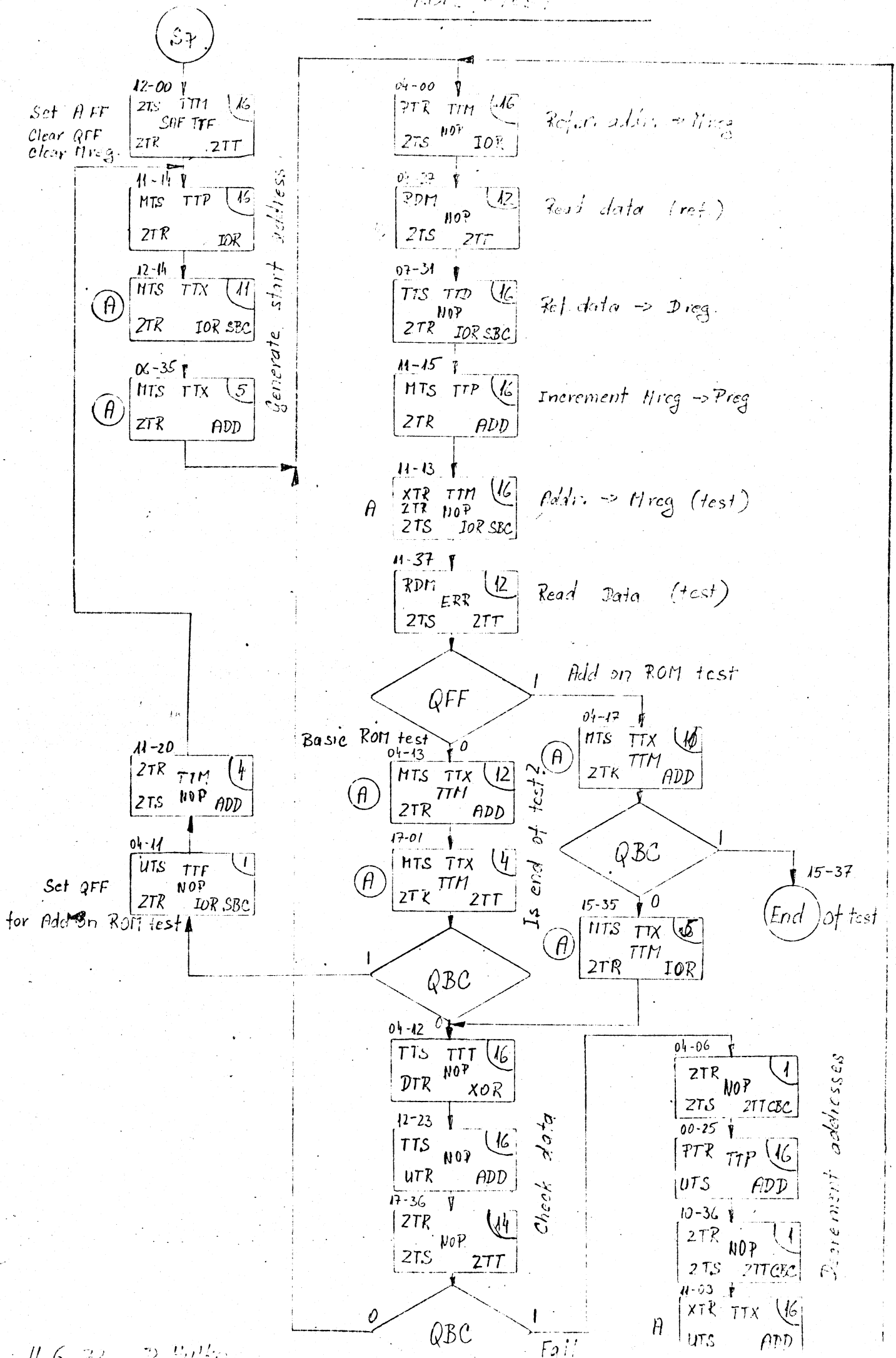
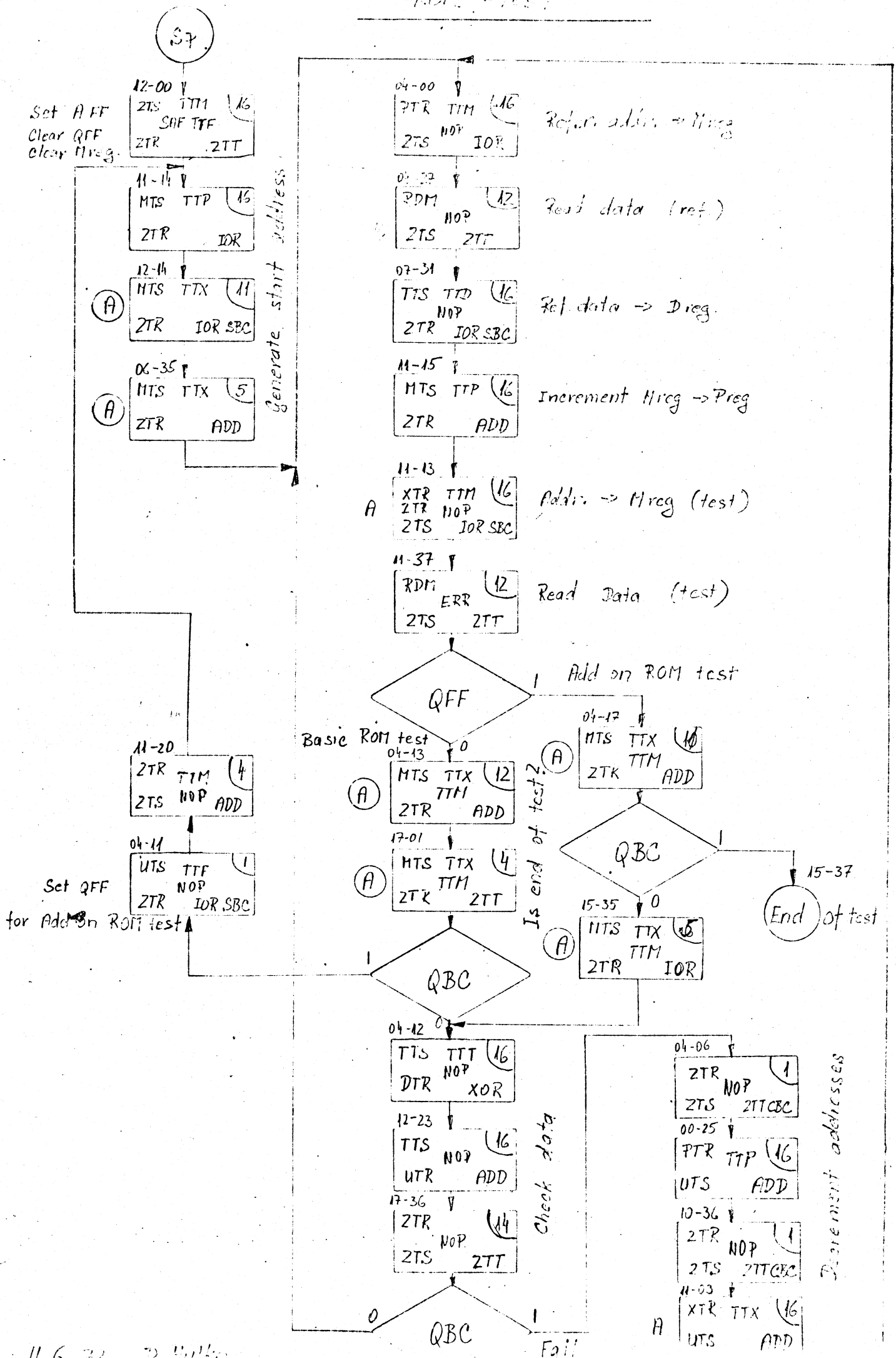






A21	Reference	Preg	A22	test	Mreg	
0000	-	0777	4000	-	4777	Basic ROM test
1000	-	1777	5000	-	5777	
2000	-	2777	6000	-	6777	
3000	-	3777	7000	-	7777	
10000	-	10777	14000	-	14777	Add on ROM test
11000	-	11777	15000	-	15777	

Slot 1

Slot 3

QFF = 0 Basic ROM test

QFF = 1 Add on ROM test

T, D reg - Data registers

M, P, A reg - Address registers

Error:

D reg. - Data

M reg. - Fail address

T reg. - Fail. bits

A24 Modification:

Remove IC 19 p. 11 and connect to IC 14 p. 10.

= I = IC 6 p. 5

= " = +5V

= II = IC 17 p. 4

= II = GND

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