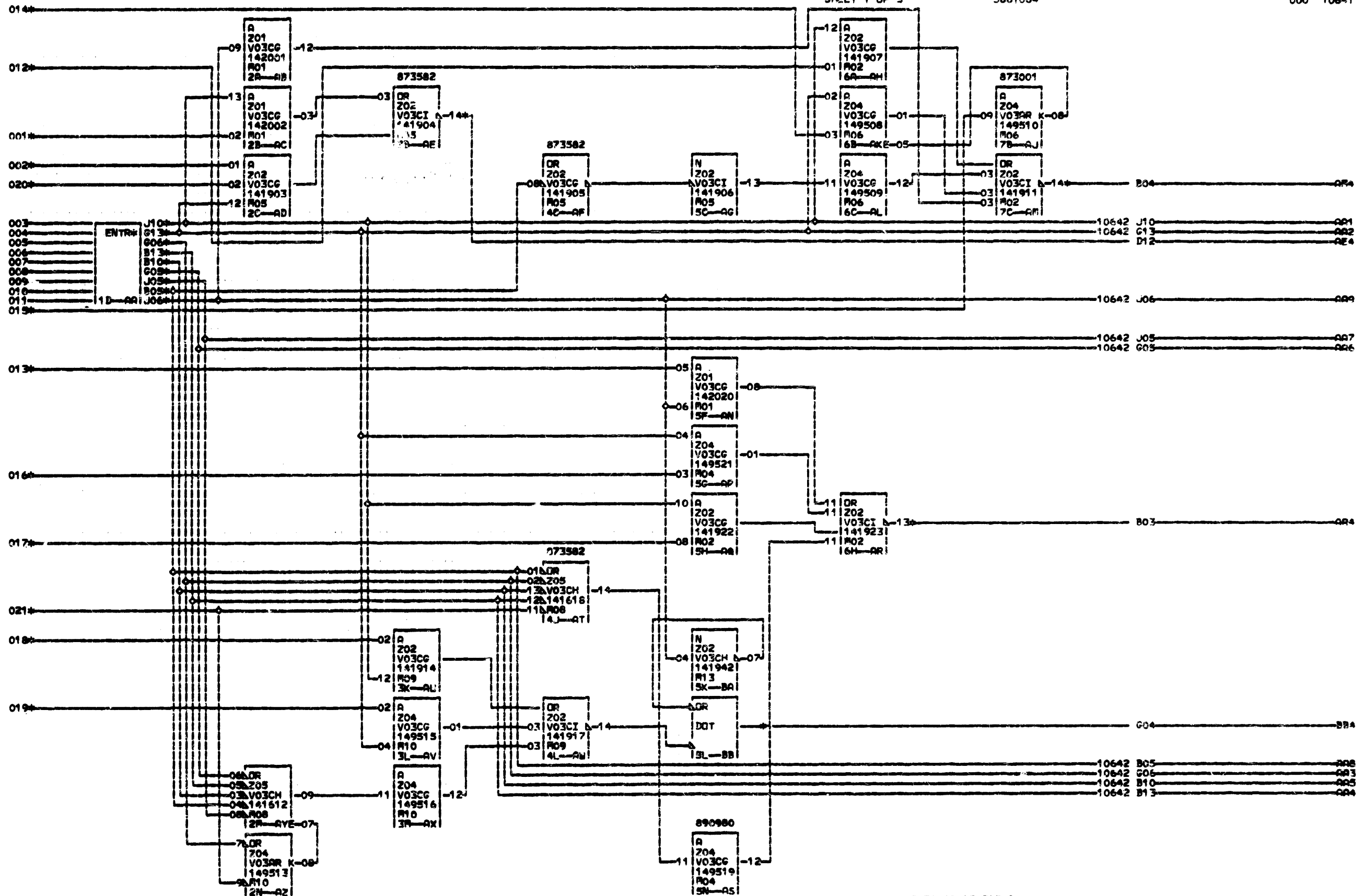


B12
B02
D02
D10
G09
J10
G13
G06
B13
B10
G05
J05
B05
J06
D11
D05
B08
D04
J02
J04
J07



CARD SIZE 2 24 PAC
VOLTAGE PINS
1 +12 D03A J03
0 GND D08A J08
6 STANDARD RESTRICTED
4 ALL VOLTAGES ARE BUSSED
1 * Z01 +12 15000 16
* Z02 +12 15000 16
000* Z03 +12 15000 16
* CONTINUED ON PAGE 2

V65

001	B12	019	J07	G13	AA7	B04
002	D11	020	G09	AA3	AA5	AA4
010	B08	021	J02	AA4	AA6	BB4
012	B02	022	J10	AA5	AA7	G04
013	D04	023	AA2	AA6	AA8	
015	J04	024	AA3	AA7	AA9	
016		025	AA4	AA8	AA0	
017		026	AA5	AA9	AA1	
018		027	AA6	AA0	AA2	
014		028	AA7	AA1	AA3	

05-30-68 174515 A

XFER REG INPUT BITS 0 TO 4	1
WRT BIT XFER TO ID REG	0
DATE 06-14-68 MACH. ASSEMBLY	6
LOG 152 FRAME	4
CD PG 1 OF 2 P.N. 5801064	1
IBM CORP. MD SLK. BC	000