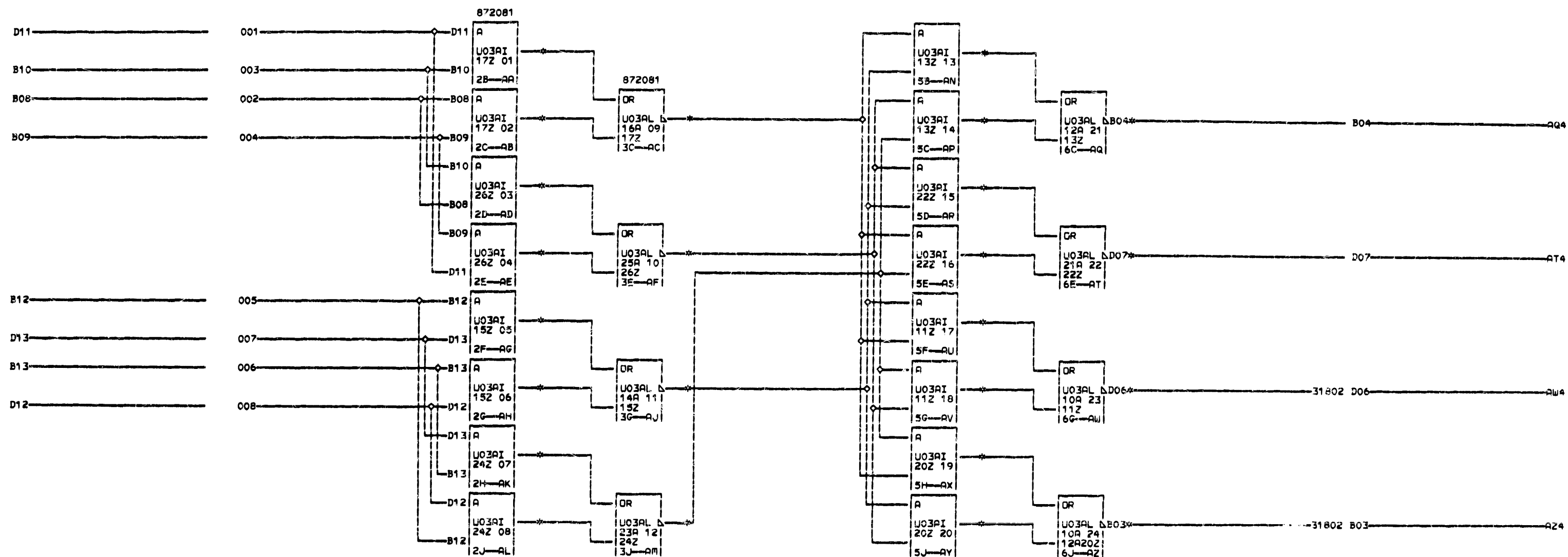




CARD SIZE 1-12 PAC
VOLTAGE PINS
3 +5V0811 +3V0D03
1 -3V0806 GND0D08
8 STANDARD RESTRICTED
0
1
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AA4 1721 13207 I 809 26202 AJ4 1521 14A1 2421 13204 I 26211 13203 I 15211 22208 I 15211 11209 2021 10A3
U03 AB4 1721 11207 N 26212 25A03 I 15207 AL4 24211 22203 I 24211 13204 I 17211 22202 I 17211 11207 A24
AC4 1721 16A1 2621 0 26211 25A04 I D13 15208 2421 0 24211 11203 N 13212 12A03 I 24211 22203 I 24211 11203 2021 12A4
I D11 17207 AE4 0 26211 13203 I B13 15203 AM4 AN4 0 24211 20208 N 13211 12A04 N 22212 21A03 I 26211 11204 I 24211 20208
I B10 17208 2621 0 26211 11204 N 15212 14A03 I D13 24208 AP4 2221 1121 0 11211 04202 N 20211 10A01
I B08 17203 AF4 AG4 0 26211 20202 N 15211 14A04 I B13 24208 1321 2221 1121 0 11211 04202 N 20211 10A01
I B09 17204 2621 1521 0 15211 13208 I D12 24202 AQ4 2221 1121 0 11211 04202 N 20211 10A01
N 17212 16A03 I B10 25209 AH4 0 15211 22208 I B12 24203 1321 12A4 AT4 2221 21A1 AW4 2021 0 20211 04207
N 17214 16A04 I B08 26208 1521 0 15211 20203 N 24211 23A04 I 15211 13208 I 26211 22209 1121 10A3 AY4 0 20211 B03

5-6-64 DEV. 3208
82864 61361
RESTRICTED
MAIN ADDER HALF SUM GENERATION
DATE 10-21-64 MACH. ASSEMBLY
LOG 193 FRAME
CD PG 1 OF 2 P.N. 58C3180
IBM CORP. PU BLK. BA