

PIN	VOLT.	PIN	VOLT.	CODE	PART NO	VALUE	QTY
J 8	GND	J 3	+ 3	Z 1	361408	SLT MODULE	7
D 8	GND	D 3	+ 3	Z 2	361476	SLT MODULE	1
G 11	+ 6	G 6	- 3	Z 3	361412	SLT MODULE	12
B 11	+ 6	B 6	- 3	Z 4	361409	SLT MODULE	1
				A 1	2390418	RC MODULE	7
				A 2	2414883	RC MODULE	4
				A 3	2390417	RC MODULE	9
				A 4	2390431	RC MODULE	4

AA-ALL CIRCUITS MUST CONFORM TO ENG.SPEC. SHOWN ON SHEET 1.
AB-ASSEMBLE TO ENGINEERING SPECIFICATION 890913
AC-CARD DOES NOT CONFORM TO DECOUPLING REQUIREMENTS IN 811800.
AD-TECH LAB EVALUATION INCOMPLETE. PART SUBJECT TO WITHDRAWAL.
ADDITIONAL USAGE TO BE AVOIDED

INTERNATIONAL BUSINESS MACHINES CORP.				
NAME	CARD ASM-MAIN ADDER 4 - 7			
	LAP			
DESIGN	08-12-63	ADD	TYPE	2070
DETAIL	08-12-63	ADD	SCALE	NONE
CHECK	07-24-64	DPO	DRAW	709C 09-23-64
APPD	07-28-64	GMM	SHEET	3 OF 10

$\begin{array}{r} 13 \\ \hline D \\ \hline 13 \end{array}$	$\begin{array}{r} 12 \\ \hline D \\ \hline 12 \end{array}$	$\begin{array}{r} 11 \\ \hline D \\ \hline 11 \end{array}$	$\begin{array}{r} 10 \\ \hline D \\ \hline 10 \end{array}$	$\begin{array}{r} 9 \\ \hline D \\ \hline 9 \end{array}$	$\begin{array}{r} 8 \\ \hline D \\ \hline 8 \end{array}$	$\begin{array}{r} 7 \\ \hline D \\ \hline 7 \end{array}$	$\begin{array}{r} 6 \\ \hline D \\ \hline 6 \end{array}$	$\begin{array}{r} 5 \\ \hline D \\ \hline 5 \end{array}$	$\begin{array}{r} 4 \\ \hline D \\ \hline 4 \end{array}$	$\begin{array}{r} 3 \\ \hline D \\ \hline 3 \end{array}$	$\begin{array}{r} 2 \\ \hline D \\ \hline 2 \end{array}$
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Diagram illustrating a 12-bit shift register with a feedback loop. The register consists of 12 stages, each with a top input, a middle output, and a bottom input. The bottom input of the first stage is connected to the middle output of the twelfth stage. A feedback loop is shown with a box containing the expression $02 \ 63 \ 23 \ + \ 33 \ A \ 2$.

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