

[illegible]

GRADUATE ENGINEER AND ARCHITECT	
STAG	JANUARY

14177 1944
 14177 1944
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3A3C

5K 1152

24

255T 5K 255T 255T 255T

[illegible]

3A33 0 0+

[illegible]

Figure 10: A circuit diagram showing a 15V AC source connected in series with a 10 ohm resistor and a 5 ohm resistor. The output voltage is measured across the 5 ohm resistor.

3A60T

15V

3A60T

3

[illegible]

LA00 T 2+
3005 55 155
RESISTOR

8	2202	200402
+3	CYBER CODE	