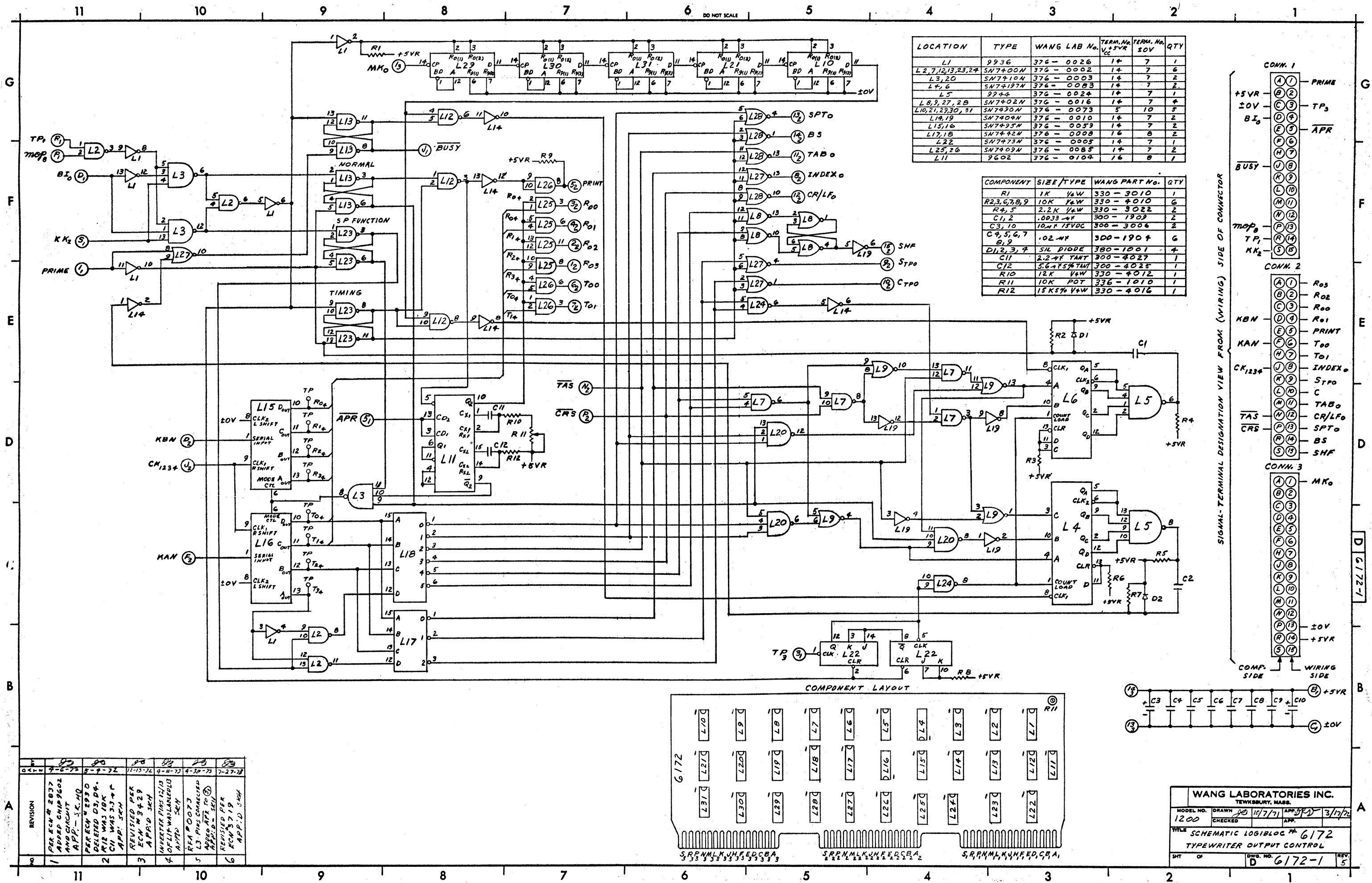
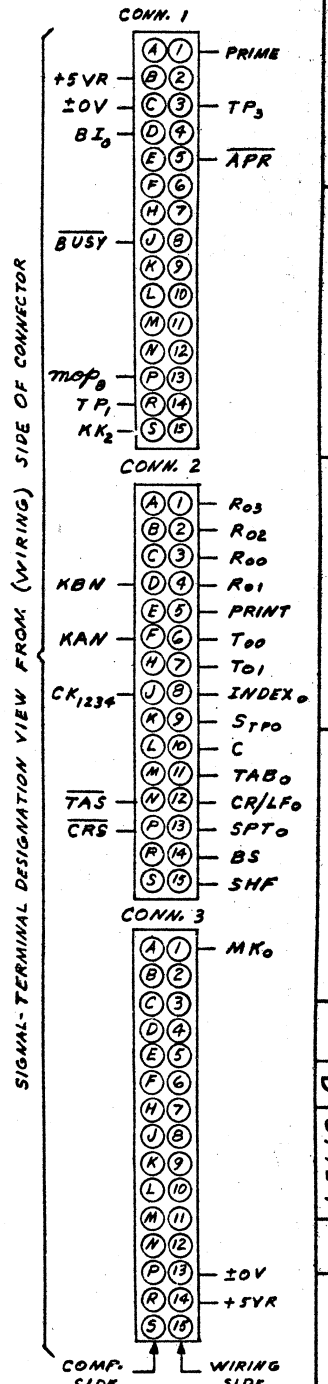




LOCATION	TYPE	WANG LAB No.	TERM. No. V _{CC}	TERM. No. 10V	QTY
L1	9936	376-0026	14	7	1
L2,7,12,13,23,24	SN7400N	376-0002	14	7	6
L3,20	SN7410N	376-0003	14	7	2
L4,6	SN74197N	376-0003	14	7	2
L5	9944	376-0024	14	7	1
L8,9,27,28	SN7402N	376-0016	14	7	4
L10,21,29,30,31	SN7490N	376-0073	5	10	5
L14,19	SN7404N	376-0010	14	7	2
L15,16	SN7495N	376-0059	14	7	2
L17,18	SN7442N	376-0008	16	8	2
L22	SN7473N	376-0005	14	7	1
L25,26	SN7409N	376-0085	14	7	2
L11	9602	376-0104	16	8	1

COMPONENT	SIZE/TYPE	WANG PART No.	QTY
R1	1K 1/2W	330-3010	1
R2,3,6,7,8,9	10K 1/2W	330-4010	6
R4,5	2.2K 1/2W	330-3022	2
C1,2	.0033-4F	300-1909	2
C3,10	10M 15VDC	300-3006	2
C4,5,6,7	.02-4F	300-1904	6
D1,2,3,4	51L DIODE	360-1001	4
C11	2.2-4F TANT	300-4027	1
C12	5.6-4F5% TANT	300-4025	1
R10	12K 1/2W	330-4012	1
R11	10K POT	336-1010	1
R12	15K 5% 1/2W	330-4016	1



REVISION	DATE	BY	REASON
1	9-6-72	88	PER ECH 2877 ADDED CHIP 9602 AND CIRCUIT APP. - 5.4.40
2	9-9-72	88	PER ECH 2877 DELETED D3, D4, R12 WAS 10K APP. 5.4.40
3	12-13-72	88	REVISOR PER ECH 2877 APP. 5.4.40
4	9-16-73	88	INVERTER PINS 12/13 OF L14 WERE Labeled APP. 5.4.40
5	4-30-75	88	REF #0073 L3 PINS CORRECTED APP. 5.4.40
6	7-27-78	88	REVISOR PER ECH 5719 APP. 5.4.40

WANG LABORATORIES INC.			
TEWKSBURY, MASS.			
MODEL NO.	DRAWN	DATE	APP. DATE
1200	10/7/71	3/17/72	
TITLE SCHEMATIC LOGIBLOC # 6172			
TYPEWRITER OUTPUT CONTROL			
SHT	OF	DWG. NO.	REV.
		6172-1	5