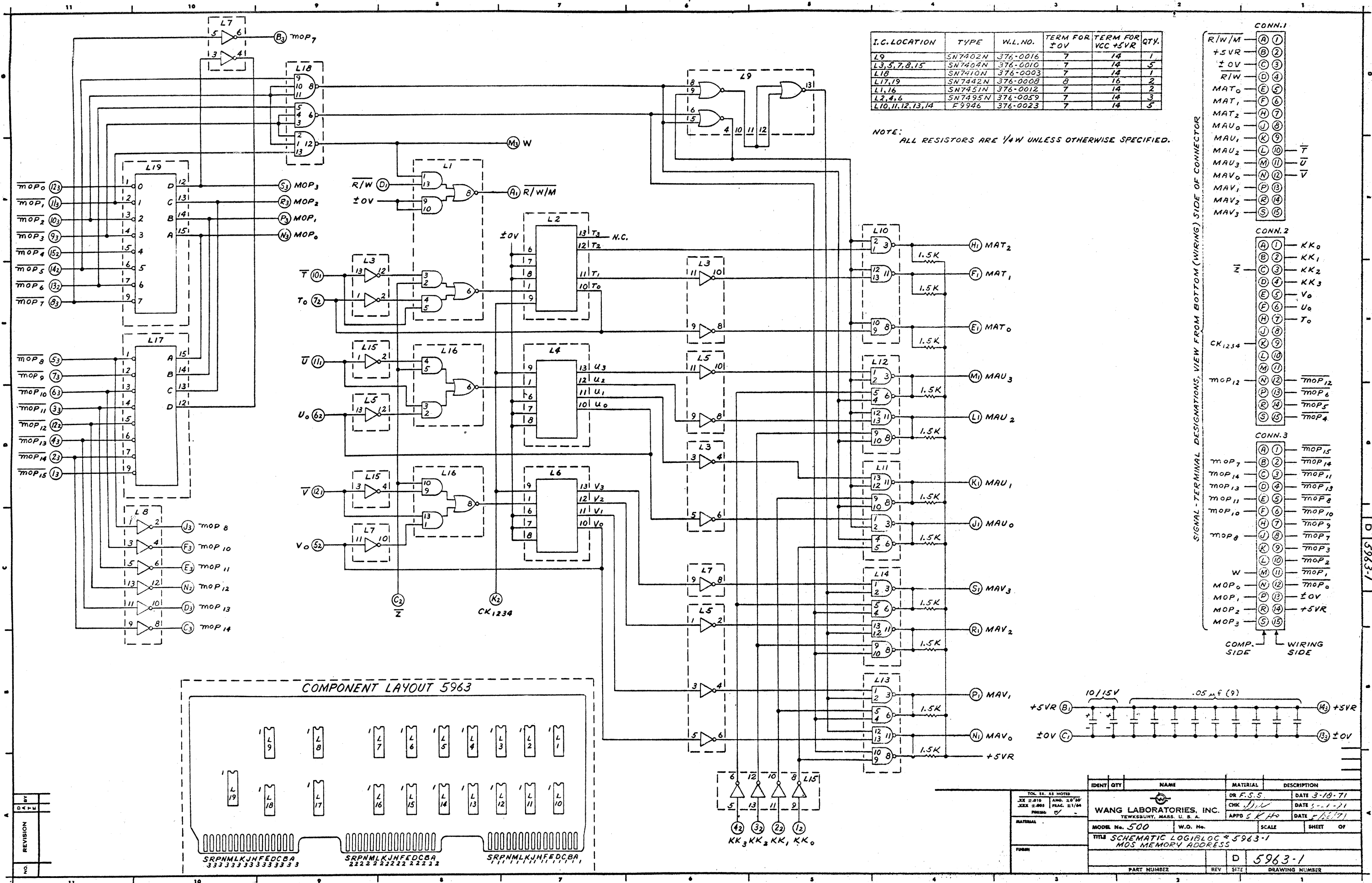
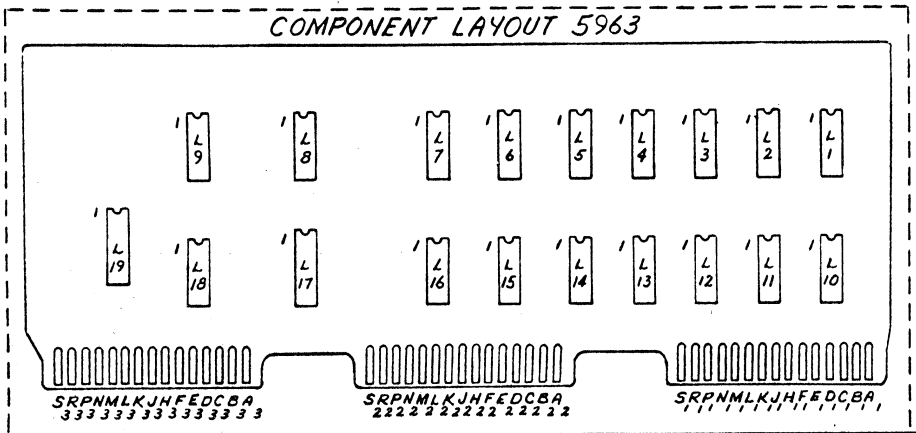
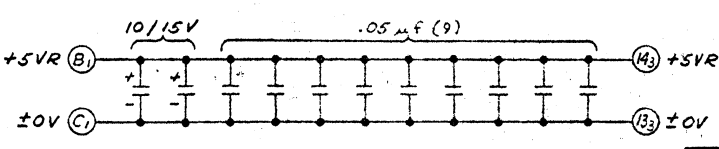
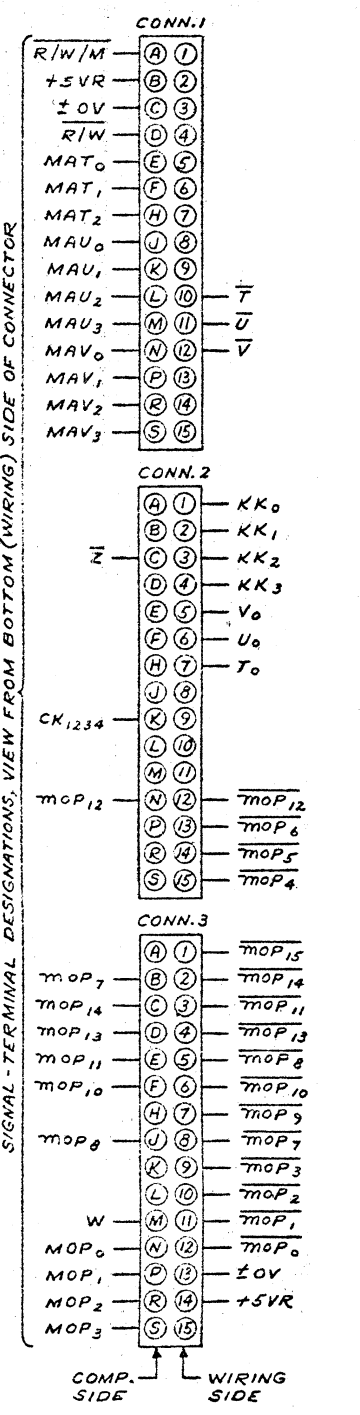


DO NOT SCALE



| I.C. LOCATION   | TYPE    | W.L. NO. | TERM FOR ±0V | TERM FOR VCC +5VR | QTY. |
|-----------------|---------|----------|--------------|-------------------|------|
| L9              | SN7402N | 376-0016 | 7            | 14                | 1    |
| L3,5,7,8,15     | SN7404N | 376-0010 | 7            | 14                | 5    |
| L18             | SN7410N | 376-0003 | 7            | 14                | 1    |
| L17,19          | SN7442N | 376-0008 | 8            | 14                | 2    |
| L1,16           | SN7451N | 376-0012 | 7            | 14                | 2    |
| L2,4,6          | SN7495N | 376-0059 | 7            | 14                | 3    |
| L10,11,12,13,14 | F9946   | 376-0023 | 7            | 14                | 5    |

NOTE:  
ALL RESISTORS ARE 1/4W UNLESS OTHERWISE SPECIFIED.



|                  |              |                                    |     |             |              |                |
|------------------|--------------|------------------------------------|-----|-------------|--------------|----------------|
| TOL. 1% AS NOTED |              | IDENT                              | QTY | NAME        | MATERIAL     | DESCRIPTION    |
| XX 2:010         | ANG. 2:0/30  | WANG LABORATORIES, INC.            |     | DR F.S.S.   | DATE 3-18-71 |                |
| XX 2:005         | PLAC. 2:1/64 | TEWKSBURY, MASS. U. S. A.          |     | CHK J.H.V.  | DATE 5-1-71  |                |
| FINISH           |              | MODEL No. 500                      |     | W.D. No.    | SCALE        | SHEET OF       |
| TYPING           |              | TITLE SCHEMATIC LOGIC LOGIC 5963-1 |     | DATE 5-1-71 |              |                |
|                  |              | MOS MEMORY ADDRESS                 |     |             |              |                |
|                  |              | PART NUMBER                        |     | REV         | SITE         | DRAWING NUMBER |